CLAIMS

What is claimed is:

- 1 1. A system for synchronizing a portable transceiver to a network, 2 comprising: a crystal oscillator; 3 a frequency synthesizer adapted to receive an output of the crystal oscillator; 4 logic coupled to the crystal oscillator, the logic configured to estimate a 5 frequency error of a received signal; and 6 a first control signal supplied from the logic to the frequency synthesizer, the 7 8 first control signal configured to adjust the frequency synthesizer to compensate for the 9 error.
- 1 2. The system of claim 1, further comprising:
 2 tuning circuitry coupled to the crystal oscillator, the tuning circuitry having a
 3 limited adjustment capability; and
 4 a second control signal supplied from the logic to the tuning circuitry, the
 5 second control signal configured to adjust the tuning circuitry, the tuning circuitry
 6 configured to compensate for the error.
- 1 3. The system of claim 2, wherein the adjustment of the frequency 2 synthesizer adjusts the timing of the portable transceiver with respect to a 3 communication network.
- 1 4. The system of claim 3, wherein the timing adjustment comprises 2 adjusting the timing of a transmitter, a receiver, a coder/decoder (CODEC) and a sleep 3 calibration element.

1	5.	The system of claim 3, wherein the tuning circuitry comprises a digital-
2	to-analog converter.	
1	6.	The system of claim 3, wherein the tuning circuitry comprises a
2	capacitance a	тау.
1	7.	The system of claim 6, wherein the capacitance array comprises fixed
2	capacitance.	
1	8.	The system of claim 6, wherein the capacitance array comprises
2	variable capacitance.	
	0	
1	9.	The system of claim 8, wherein the adjustment capability of the
2	capacitance array can tune the system to between ±2 parts per million (ppm) and	
3	±2.5ppm with	respect to the frequency and timing of the communication network.
	4.0	
1	10.	A method for synchronizing a portable transceiver to a network,
2	comprising:	
3	determining a frequency error of a signal received by the portable transceiver;	
4	and	
5	if the	frequency error is less than a predetermined value, adjusting the
6	frequency of the system by adjusting a frequency synthesizer to compensate for the	
7	error.	
1	11.	The method of claim 10, further comprising:
2		adjusting the frequency of the crystal oscillator by adjusting a tuning

circuit associated with the crystal oscillator.

3

1 12. The method of claim 11, wherein the adjustment of the frequency synthesizer adjusts the timing of the portable transceiver with respect to a communication network.

- 1 13. The method of claim 12, wherein the timing adjustment comprises 2 adjusting the timing of a transmitter, a receiver, a coder/decoder (CODEC) and a sleep 3 calibration element.
- 1 14. The method of claim 12, further comprising using a digital-to-analog converter (DAC) to adjust the frequency of the crystal oscillator.
- 1 15. The method of claim 12, further comprising using a capacitance array to adjust the frequency of the crystal oscillator.
- 1 16. The method of claim 15, further comprising using a fixed capacitance 2 array.
- 1 17. The method of claim 15, further comprising using a variable capacitance array.
- 1 18. The method of claim 17, wherein the adjustment capability of the variable capacitance array tunes the system frequency to between ±2 parts per million (ppm) and ±2.5ppm of the frequency of the communication network.
- 1 19. A system for synchronizing a portable transceiver to a network, 2 comprising:
- means for determining a frequency error of a signal received by the portable transceiver; and

03SKY0033

1 means for adjusting the frequency of the system by adjusting a frequency 2 synthesizer to compensate for the error if the frequency error is less than a 3 predetermined value.

- 1 20. The system of claim 19, further comprising: 2 means for adjusting the frequency of the crystal oscillator by adjusting a tuning circuit associated with the crystal oscillator. 3
- 21. The system of claim 20, wherein the adjustment of the frequency 1 synthesizer adjusts the timing of the portable transceiver with respect to a 2 3 communication network.
- 22. The system of claim 21, wherein the timing adjustment comprises 1 2 adjusting the timing of a transmitter, a receiver, a coder/decoder (CODEC) and a sleep calibration element. 3
- 23. The system of claim 21, wherein the means for adjusting the frequency 1 2 of the crystal oscillator comprises a digital-to-analog converter (DAC).
- 24. The system of claim 21, wherein the means for adjusting the frequency 1 of the crystal oscillator comprises a capacitance array. 2
- 25. 1 The system of claim 24, wherein the capacitance array comprises a 2 fixed capacitance array.
- 26. The system of claim 24, wherein the capacitance array comprises a 1 variable capacitance array. 2

Patent N&R Docket No. 19308.0027U1 03SKY0033

- 1 27. The system of claim 26, wherein the adjustment capability of the
- 2 capacitance array tunes the system frequency to between ±2 parts per million (ppm)
- 3 and ±2.5ppm of the frequency of the communication network.